

**Claims**

1. A converter from ECL to CMOS having an input stage, a level shifter stage, and an output stage, wherein the level shifter stage includes an NFET differential stage.
2. The converter from ECL to CMOS according to claim 1, wherein the input stage and/or the level shifter stage have a switching-threshold control system.
3. The converter from ECL to CMOS according to claim 2, wherein the converter comprises means for generating a reference voltage for current-source transistors to control the switching threshold.
4. The converter from ECL to CMOS according to claim 3, wherein the means for generating a reference voltage comprise a simulating network for portions of the converter to determine the reference voltage.
5. A network element for transmitting signals which comprises a converter from ECL to CMOS having an input stage, a level shifter stage, and an output stage, wherein the level shifter stage includes an NFET differential stage.